



92 B

T&A-110

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

T. MIYAMOTO et al

Serial No. 10/046,446

Group Art Unit: 2811

Filed: January 16, 2002

Examiner: D. Owens

For: SEMICONDUCTOR DEVICE AND MANUFACTURING  
METHOD OF THAT

REQUEST TO CORRECT NOTICE OF ALLOWANCE

Commissioner for Patents  
Mail Stop Issue Fee  
Alexandria, VA 22313-1450

Sir:

Please correct the Notice of Allowance mailed May 6, 2004, as follows.

The title should read: --WAFER LEVEL CHIP SIZE PACKAGE HAVING REROUTING LAYERS--.

The undersigned requests that a corrected Notice of Allowance be issued.

Respectfully submitted,



John R. Mattingly  
Registration No. 30,293  
Attorney for Applicants

MATTINGLY, STANGER & MALUR, P.C.  
1800 Diagonal Road, Suite 370  
Alexandria, Virginia 22314  
Telephone: (703) 684-1120  
Date: May 13, 2004